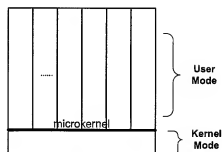


Layered monolithic kernel design



Microkernel design

Figure 1

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**Waiting on
a fast
mutex**

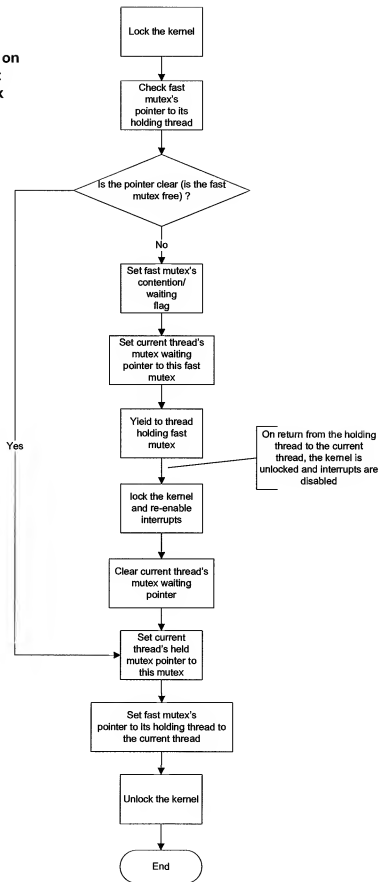


Figure 2

Releasing a fast mutex

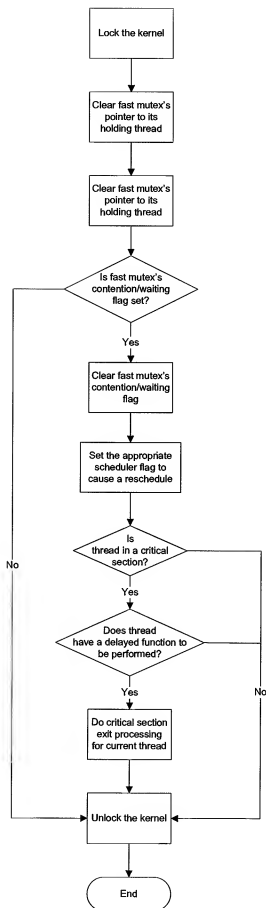
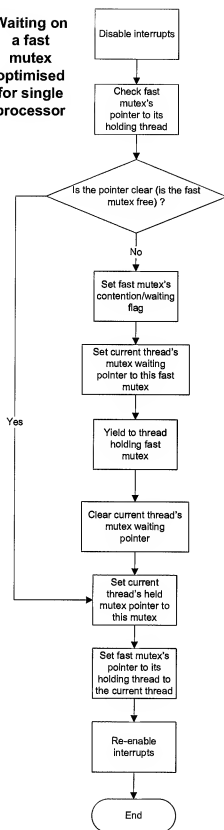


Figure 3

**Waiting on
a fast
mutex
optimised
for single
processor**



**Releasing a
fast mutex
optimised
for single
processor**

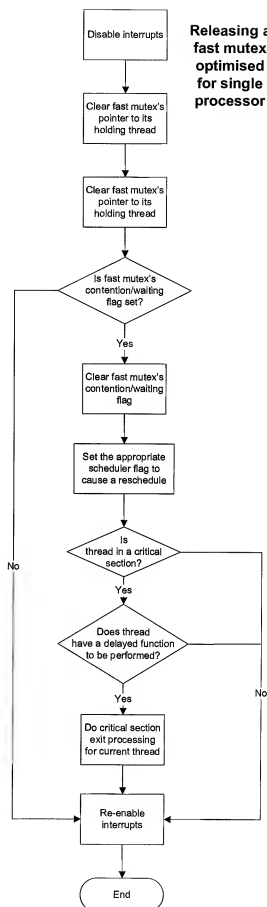


Figure 4